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(Not for submission under 37 CFR 1.99)

Application Number	09989762
Filing Date	2001-11-19
First Named Inventor	Kenneth Y. Ogami
Art Unit	2123
Examiner Name	Osborne, Luke R.
Attorney Docket Number	CYPR-CD01175M

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Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	6460172		2002-10-01	Insenser Farre , et al.	
	2	6499134		2002-12-24	Buffet , et al.	
	3	6529791		2003-03-04	Takagi	
	4	6592626		2003-07-15	Bauchot , et al.	
	5	6611952		2003-08-26	Prakash , et al.	
	6	6613098		2003-09-02	Sorge , et al.	
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	8	6711731		2004-03-23	Weiss	

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10	6724220		2004-04-20	Snyder , et al.	
11	6792584		2004-09-14	Eneboe , et al.	
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13	6825869		2004-11-30	Jiang	
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	1	20020091739	A1	2002-07-11	Ferlitsch, Andrew Rodney ; et al.	

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	2	20020188310	A1	2002-12-12	Zizzo, Claudio	
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	1	0308583	EP	A2	1989-03-29	AUTOMATION SYST		<input type="checkbox"/>
	2	0450863	EP	A2	1991-10-09	PILKINGTON MICRO ELECTRONICS		<input type="checkbox"/>
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	1	Kory Hopkins; "DEFINITION"; 01/16/97; http://www.cs.sfu.ca/cs/people/GradStudent/sonal/papers/graph_isomorphism/node2.html	<input type="checkbox"/>

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2	C. Ebeling et al.; "VALIDATING VLSI CIRCUIT LAYOUT BY WIRELIST COMPARISON"; Sept. 1983; In proceedings of the IEEE International Conference on Computer Aided Design (ICCAD-83), pp. 172-173	<input type="checkbox"/>
3	"THE GEMINI NETLIST COMPARISON PROJECT"; http://www.cs.washington.edu/research/projects/lis/www/gemini/gemini.html , larry@cs.washington.edu	<input type="checkbox"/>
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6	CYPR-CD00169; "PROGRAMMABLE MICROCONTROLLER ARCHITECTURE (MIXEDANALOG/DIGITAL)"; 08/07/01; 09/924,734; Snyder et al.	<input type="checkbox"/>
7	CYPR-CD00170; "DIGITAL CONFIGURABLE MACRO ARCHITECTURE"; 07/18/01; 09/909,045; W. Snyder	<input type="checkbox"/>
8	CYPR-CD00172; "CONFIGURING DIGITAL FUNCTIONS IN A DIGITAL CONFIGURABLE MACRO ARCHITECTURE"; 07/18/01; 09/909,109; W. Snyder	<input type="checkbox"/>
9	CYPR-CD00173; "A PROGRAMMABLE ANALOG SYSTEM ARCHITECTURE (AS AMENDED)"; 07/18/01; 09/909,047; M. Mar	<input type="checkbox"/>
10	CYPR-CD00174; "PROGRAMMING METHODOLOGY AND ARCHITECTURE FOR A PROGRAMMABLE ANALOG SYSTEM (AS AMENDED)"; 08/14/01; 09/930,021; Mar et al.	<input type="checkbox"/>
11	CYPR-CD00175; "METHOD FOR SYNCHRONIZING AND RESETTNG CLOCK SIGNALS SUPPLIED TO MULTIPLE PROGRAMMABLE ANALOG BLOCKS (AS AMENDED)"; 10/01/01; 09/969,311; B. Sullam	<input type="checkbox"/>
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13	CYPR-CD00182; "IN-SYSTEM CHIP EMULATOR ARCHITECTURE"; 10/10/01; 09/975,115; Snyder et al.	<input type="checkbox"/>
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